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Commissioner for Patents  
P.O. BOX 1450, Alexandria, Virginia 22313-1450

Examiner: P. Hassanzadeh  
Group Art Unit: 1763  
Confirmation No. 7716

TRANSMITTED TO USPTO MAIN FACSIMILE NO. 703-872-9306

RE: H. KITSUNAI, et al. - U.S. Application No. 10/825,114  
Att'y Docket No. 500.40633VX1

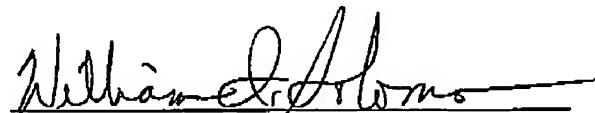
**SUBMISSION OF: AMENDMENT AFTER FINAL REJECTION**

Sir:

Applicants hereby submit the attached Amendment After Final Rejection (13pp.) for entry in the above-noted application. Expedited procedure is respectfully requested under 37 CFR 1.116.

**CERTIFICATE OF TRANSMISSION:**

I hereby certify that the attached **AMENDMENT AFTER FINAL REJECTION** (13 pp.) is being **FORMALLY TRANSMITTED** via the USPTO Main Facsimile No. 703-872-9306 on April 14, 2005.

  
William I. Solomon

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500.40633VX1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: H. KITSUNAI, et al.  
Application No.: 10/825,114  
Filed: April 16, 2004  
For: PROCESS FOR PRODUCING SEMICONDUCTOR DEVICE  
Group: 1763  
Examiner: P. Hassanzadeh  
Confirmation No.: 7716

**AMENDMENT AFTER FINAL REJECTION**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 14, 2005

Sir:

In response to the Office Action mailed January 14, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.